



# MS-7297 Ver:21

## CPU:

AMD K8 AM2 Athlon 64/Athlon 64 FX

## System Chipset:

ATI RS485

ATI SB600

## On Board Chipset:

Winbond Super I/O -- W83627EHG Ver.H

LAN -- RTL8100C/RTL8110SC

HD Codec --ALC861

BIOS --LPC FLASH ROM 4M

## Main Memory:

DDR2 \* 2 (Max 4GB)

## Expansion Slots:

PCI-E X 1 \*1

PCI-E X 16 \*1

PCI 2.3 Slot X 2

## PWM:

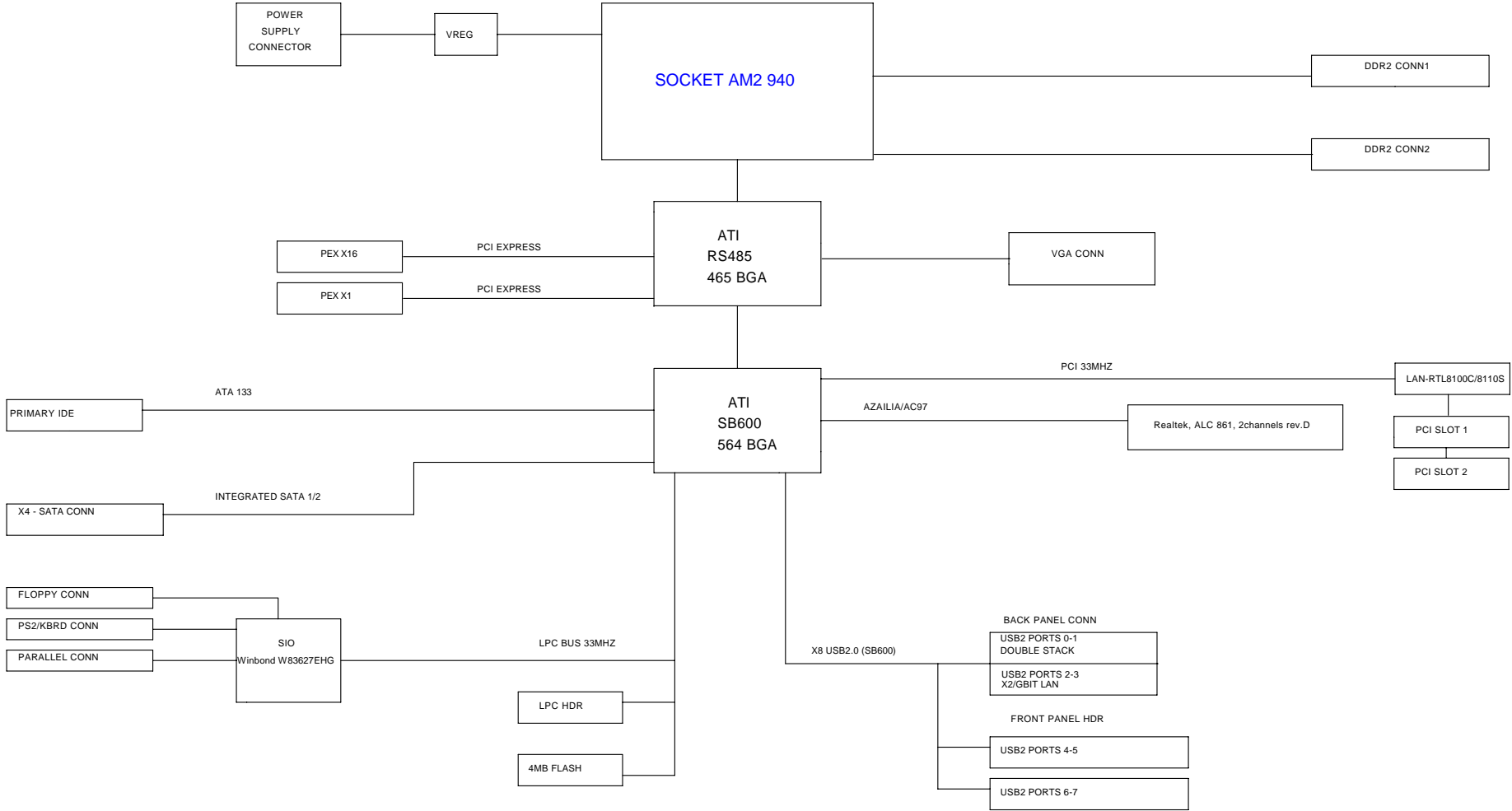
Controller--Intersil ISL6566CR 3 Phase

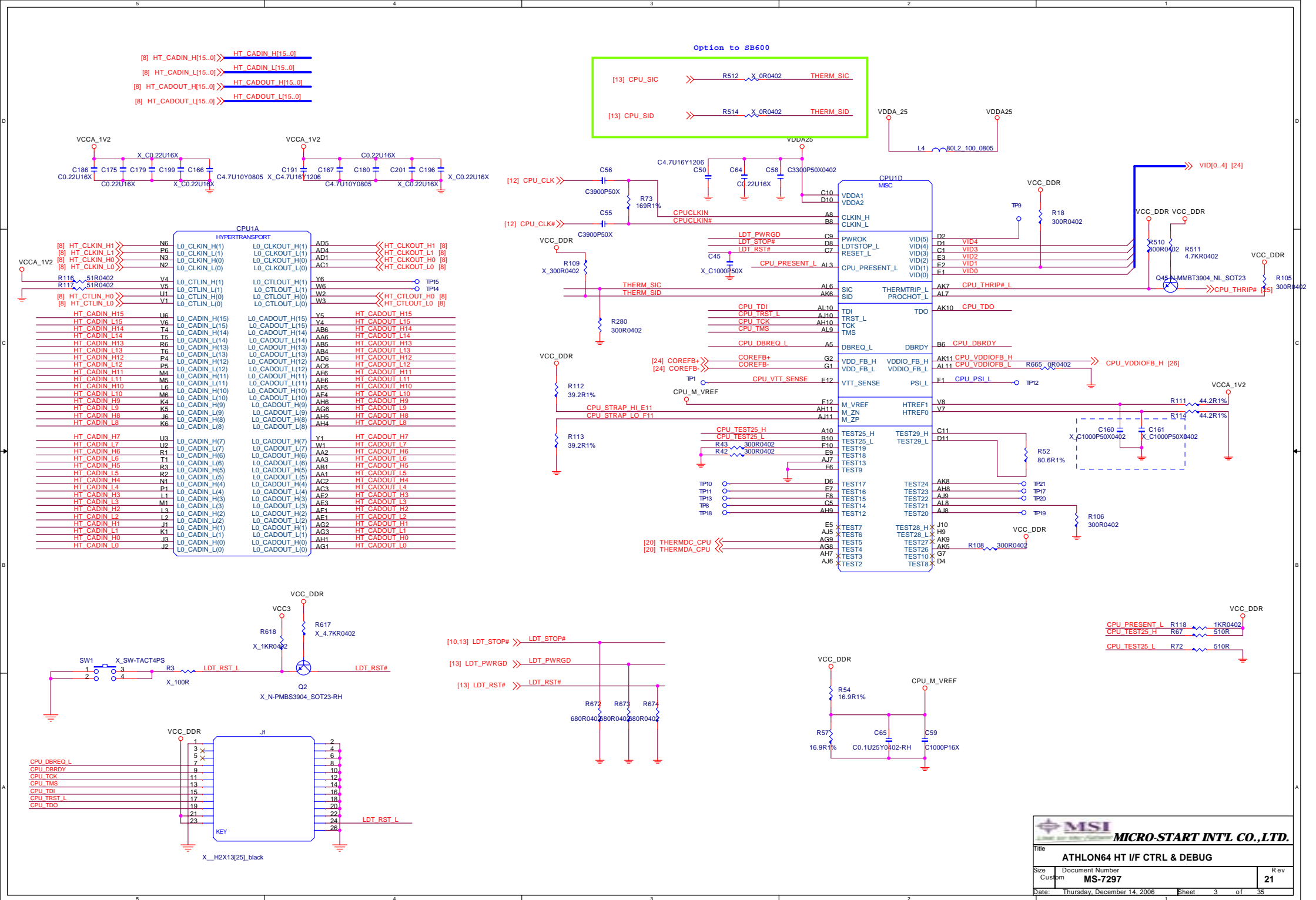
## Clock Generator:

Controller--ICS 951464AGLF

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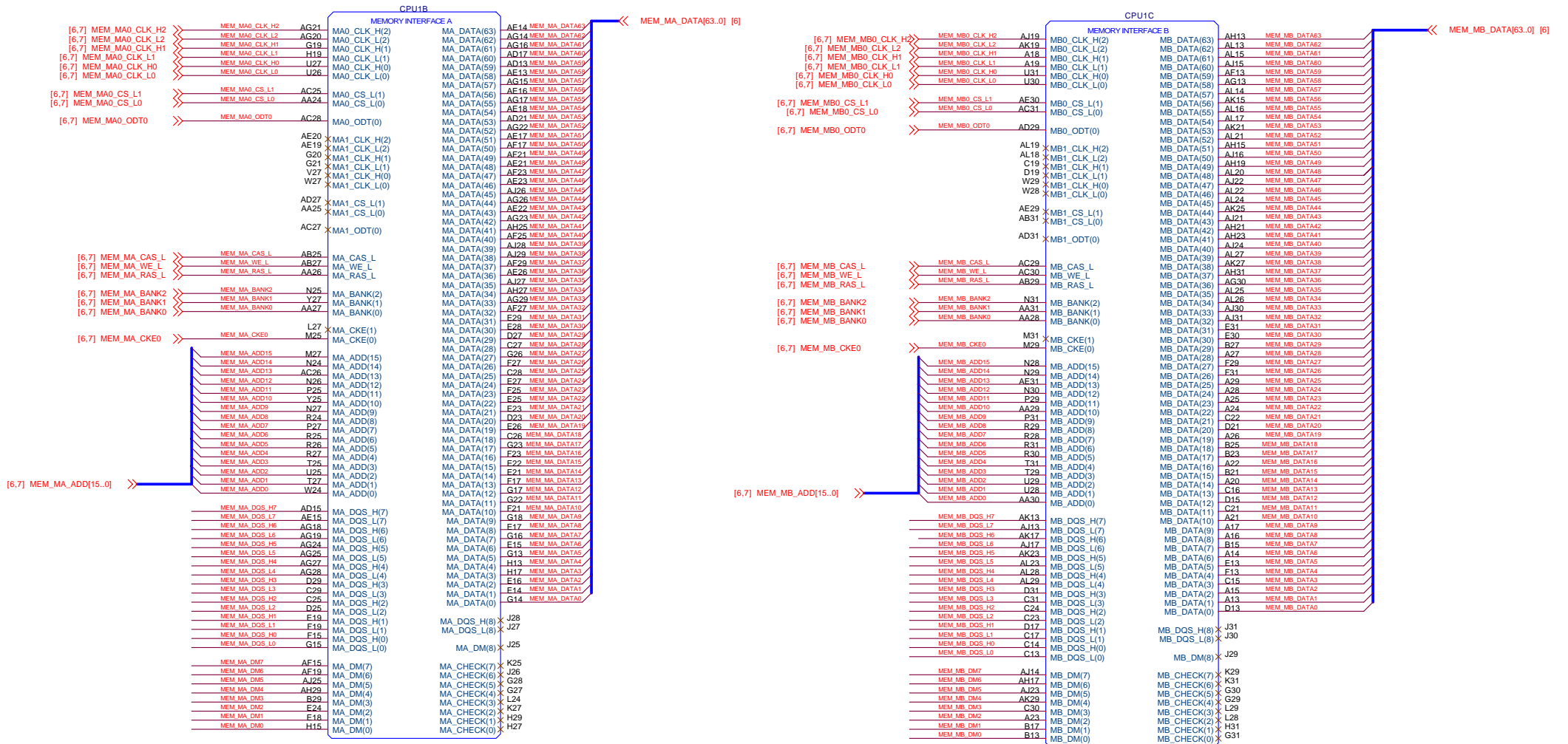
BLOCK DIAGRAM

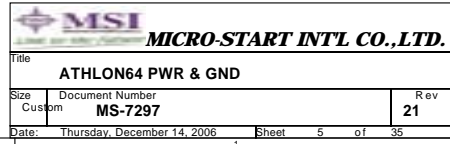




[6] MEM\_MA\_DQS\_L[7..0] >> \_\_\_\_\_  
[6] MEM\_MA\_DQS\_H[7..0] >> \_\_\_\_\_  
[6] MEM\_MA\_DM[7..0] >> \_\_\_\_\_

[6] MEM\_MB\_DQS\_L[7..0] >> \_\_\_\_\_  
[6] MEM\_MB\_DQS\_H[7..0] >> \_\_\_\_\_  
[6] MEM\_MB\_DM[7..0] >> \_\_\_\_\_





[4] MEM\_MA\_DQS\_H[7..0] >>>  
[4] MEM\_MA\_DQS\_L[7..0] >>>

[4] MEM\_MA\_DM[7..0] >>>

[4,7] MEM\_MA\_BANK0 >>>  
[4,7] MEM\_MA\_BANK1 >>>  
[4,7] MEM\_MA\_BANK2 >>>  
[4,7] MEM\_MA\_BANK3 >>>

[4,7] MEM\_MA\_ADD[15..0] >>>

[4,7] MEM\_MA0\_CLK\_H0 >>>  
[4,7] MEM\_MA0\_CLK\_L0 >>>  
[4,7] MEM\_MA0\_CLK\_H1 >>>  
[4,7] MEM\_MA0\_CLK\_L1 >>>  
[4,7] MEM\_MA0\_CLK\_H2 >>>  
[4,7] MEM\_MA0\_CLK\_L2 >>>

[4,7] MEM\_MA\_CKE0 >>>

[4,7] MEM\_MA\_RAS\_L >>>

[4,7] MEM\_MA\_CAS\_L >>>

[4,7] MEM\_MA0\_CS\_L0 >>>

[4,7] MEM\_MA0\_CS\_L1 >>>

DIMM 1  
ADDR=1010000B

[4] MEM\_MB\_DQS\_H[7..0] >>>  
[4] MEM\_MB\_DQS\_L[7..0] >>>

[4] MEM\_MB\_DM[7..0] >>>

[4,7] MEM\_MB\_BANK0 >>>  
[4,7] MEM\_MB\_BANK1 >>>  
[4,7] MEM\_MB\_BANK2 >>>  
[4,7] MEM\_MB\_BANK3 >>>

[4,7] MEM\_MB\_ADD[15..0] >>>

[4,7] MEM\_MB0\_CLK\_H0 >>>  
[4,7] MEM\_MB0\_CLK\_L0 >>>  
[4,7] MEM\_MB0\_CLK\_H1 >>>  
[4,7] MEM\_MB0\_CLK\_L1 >>>  
[4,7] MEM\_MB0\_CLK\_H2 >>>  
[4,7] MEM\_MB0\_CLK\_L2 >>>

[4,7] MEM\_MB\_CKE0 >>>

[4,7] MEM\_MB\_RAS\_L >>>

[4,7] MEM\_MB\_CAS\_L >>>

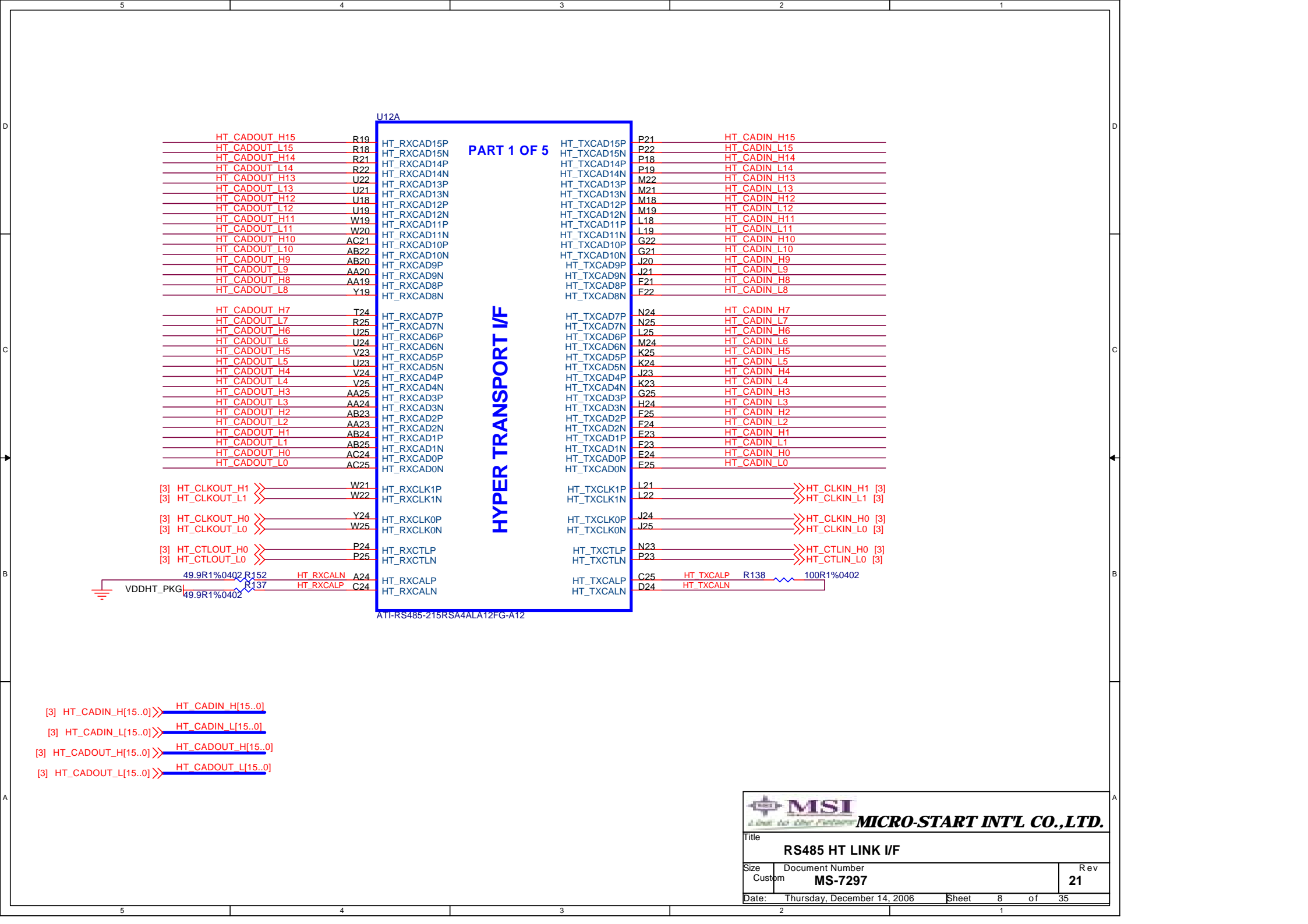
[4,7] MEM\_MB0\_CS\_L0 >>>

[4,7] MEM\_MB0\_CS\_L1 >>>

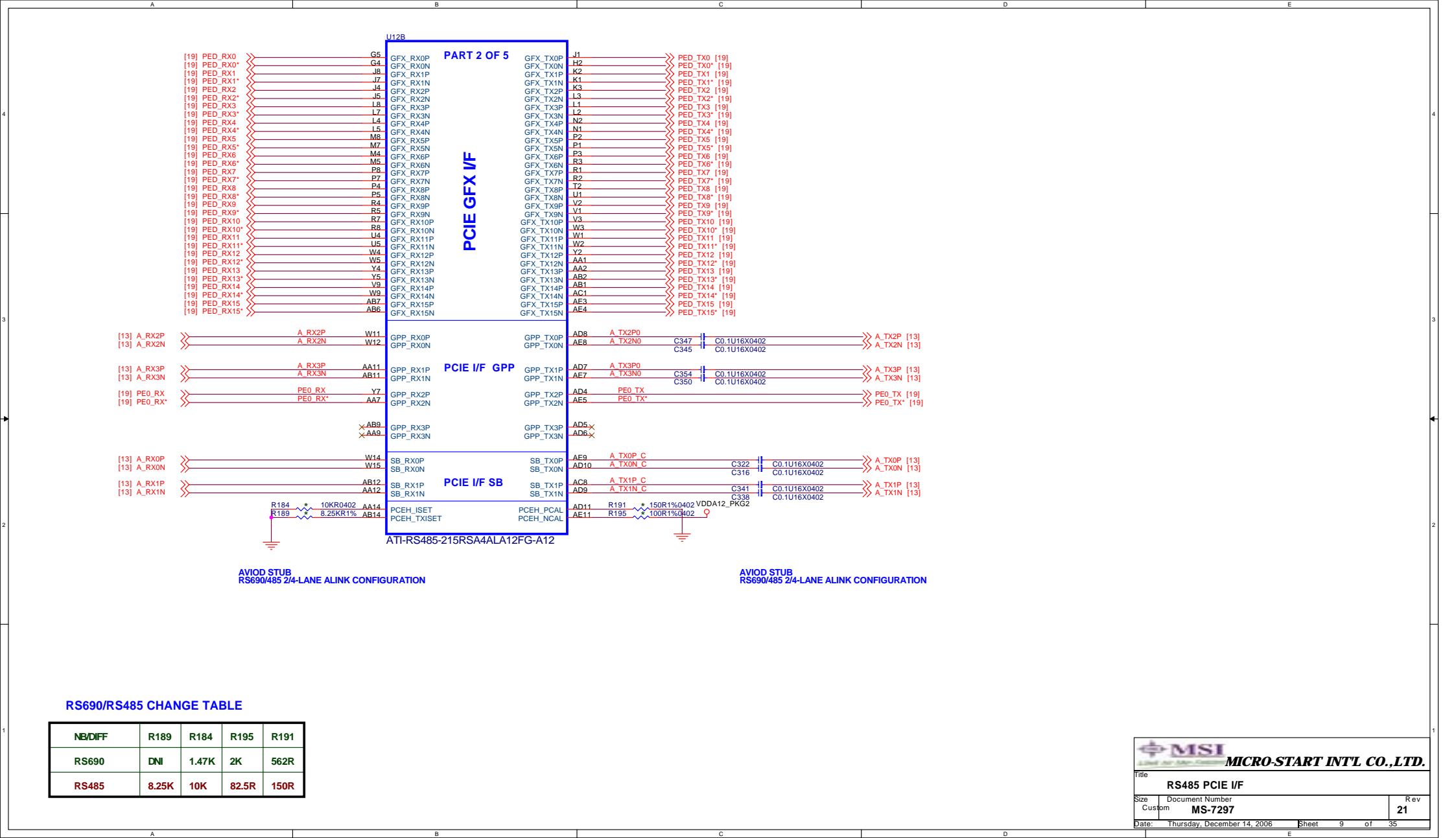
DIMM 2  
ADDR=1010001B

[12,14,20,21,25] SCL <<< SCL  
[12,14,20,21,25] SDA <<< SDA

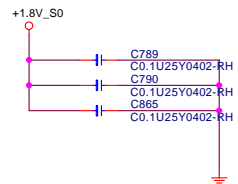




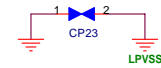




FOR EMI



NOTE: CONNECT TO GND CLOSE TO FIRST CAP



U12C

PART 3 OF 5

CRT/TVOUT

LVTM

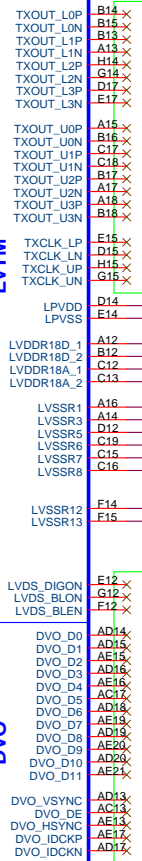
PLL PWR

PM

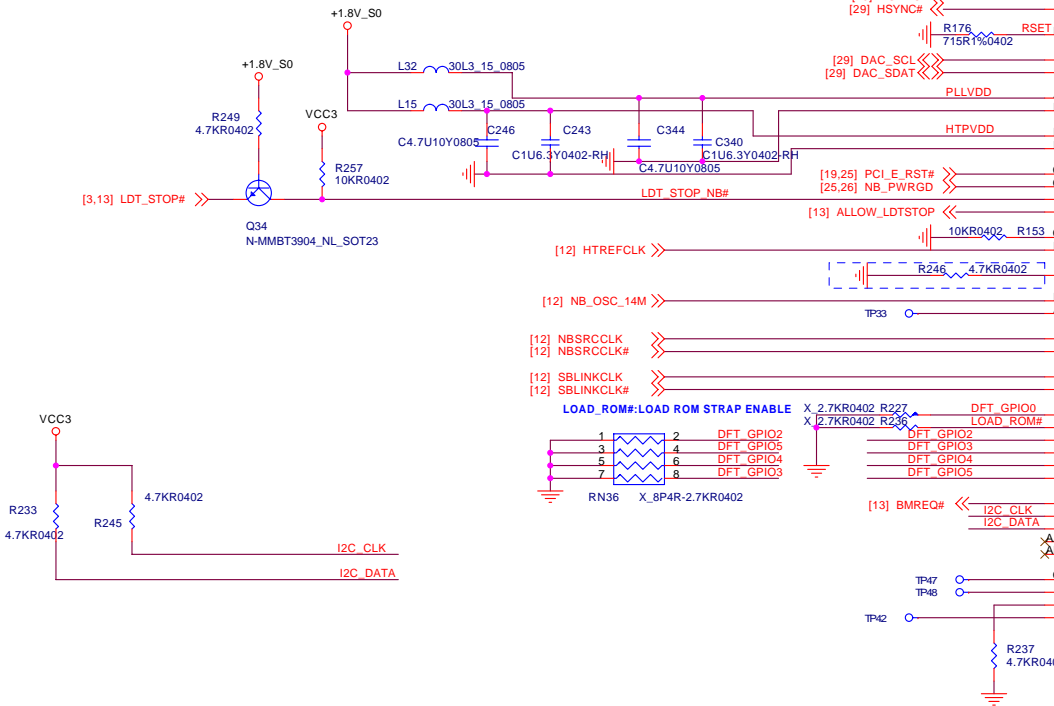
CLOCKS

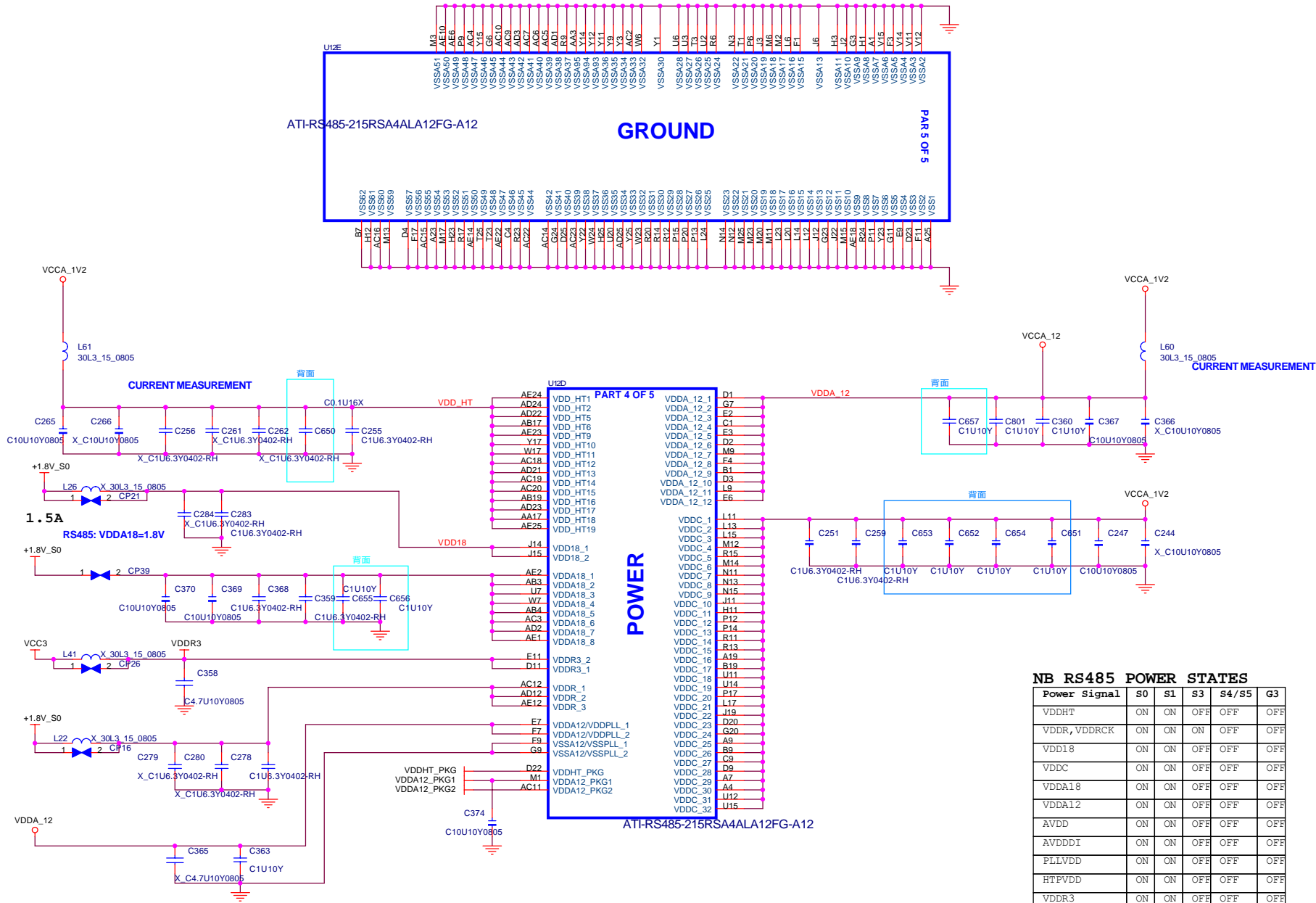
MIS.

DVO

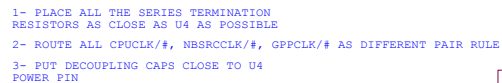


ATI-RS485-215RSA4ALA12FG-A12






NB RS485 POWER STATES					
Power Signal	S0	S1	S3	S4/S5	G3
VDDHT	ON	ON	OFF	OFF	OFF
VDDR, VDDRCK	ON	ON	ON	OFF	OFF
VDD18	ON	ON	OFF	OFF	OFF
VDDC	ON	ON	OFF	OFF	OFF
VDDA18	ON	ON	OFF	OFF	OFF
VDDA12	ON	ON	OFF	OFF	OFF
AVDD	ON	ON	OFF	OFF	OFF
AVDDDI	ON	ON	OFF	OFF	OFF
PLLVD	ON	ON	OFF	OFF	OFF
HTPVDD	ON	ON	OFF	OFF	OFF
VDDR3	ON	ON	OFF	OFF	OFF
LPVDD	ON	ON	OFF	OFF	OFF
LVDDR18D	ON	ON	OFF	OFF	OFF
LVDDR18A	ON	ON	OFF	OFF	OFF

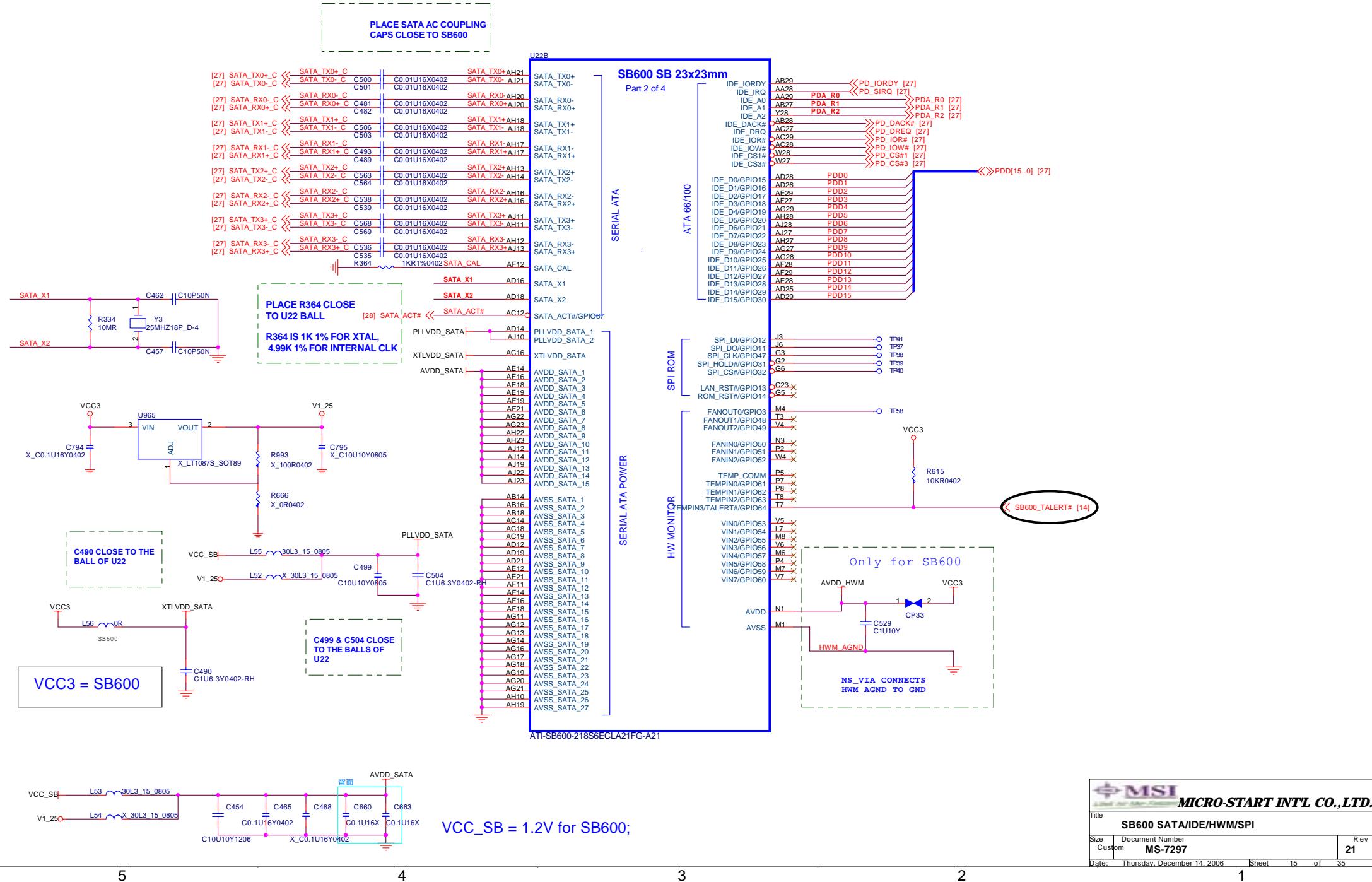


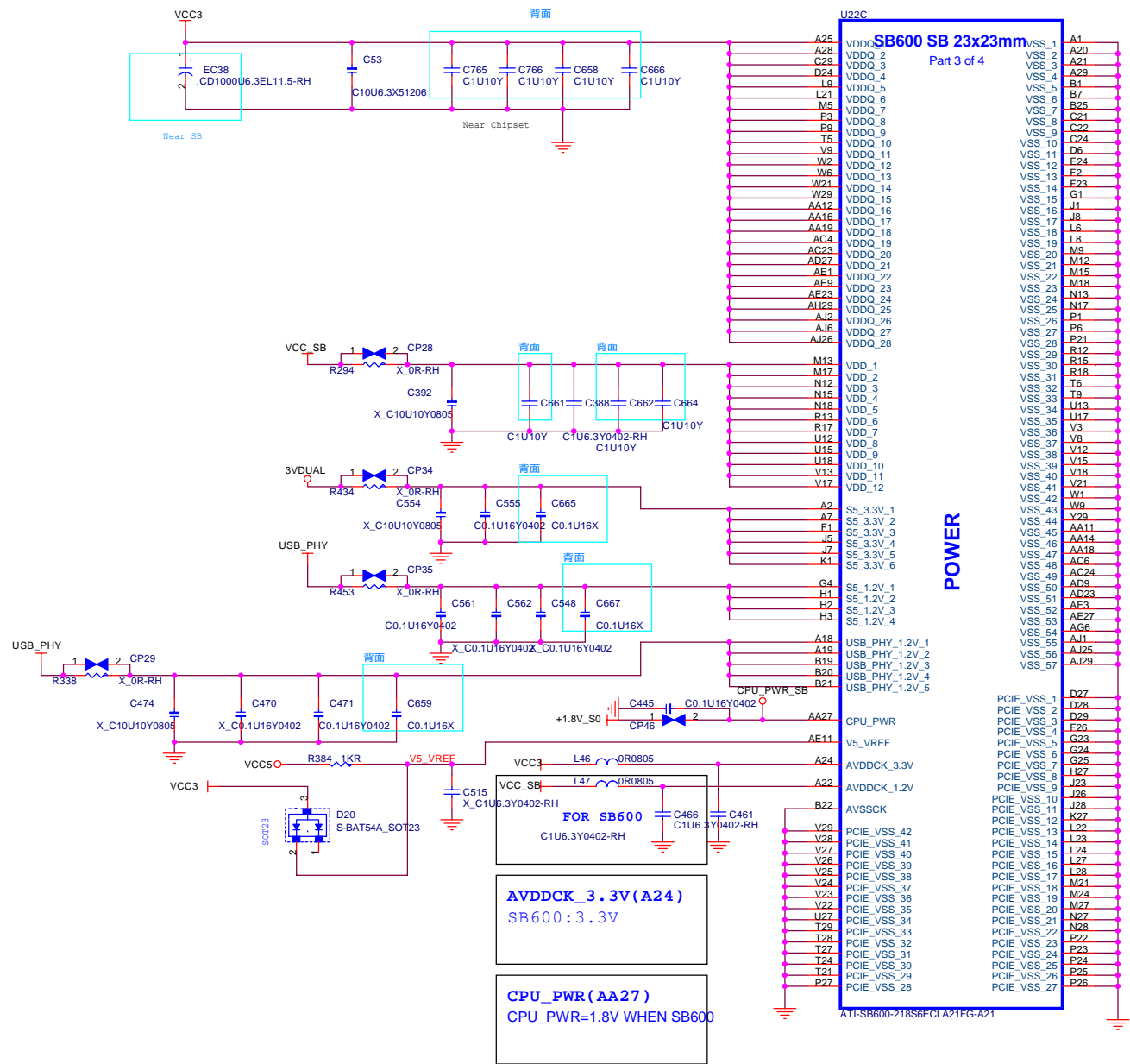
GFXXCLK	GFXXCLK#	GFXXCLK [19]	GFXXCLK# [19]
NBSRCLK	NBSRCLK#	NBSRCLK [10]	NBSRCLK# [10]
SBSRCLK	SBSRCLK#	SBSRCLK [13]	SBSRCLK# [13]
SBCLK	SBCLK#	SBCLK [10]	SBCLK# [10]
GPCLK0	GPCLK0#	GPCLK0 [19]	GPCLK0# [19]

 <b>MICRO-START INTL CO.,LTD.</b>			
Title			
<b>Clock Generator ICS951464 &amp; SLG84607</b>			
Size	Document Number		Rev
Custom	<b>MS-7297</b>		<b>21</b>
Date:	Thursday, December 14, 2006	Sheet	12 of 35









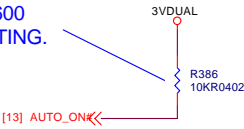
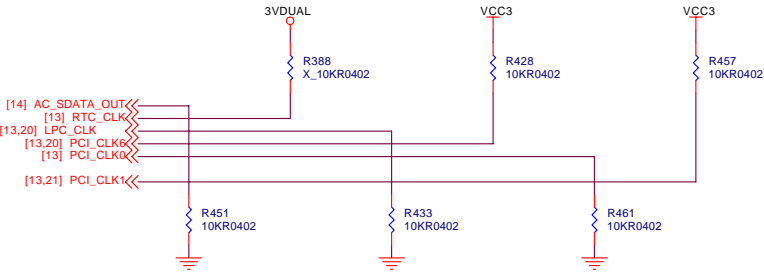




# REQUIRED STRAPS

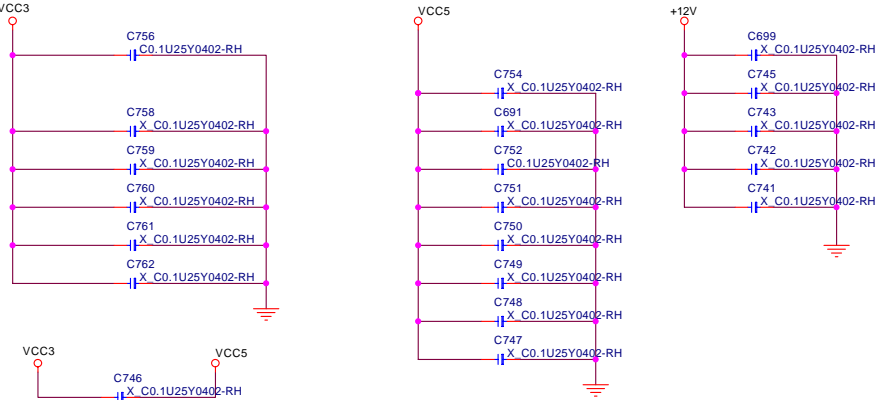
SB600 HAS 15K INTERNAL PD FOR AC\_SDATA\_OUT,  
15K PU FOR RTC\_CLK, EXTERNAL PU/PD IS  
NOT REQUIRED; FOR SB460, EXTERNAL PU/PD ARE  
REQUIRED

NOTE: R386 PU RESISTOR FOR  
RTC\_IRQ# IS REQUIRED FOR SB600  
TO KEEP THE INPUT FROM FLOATING.

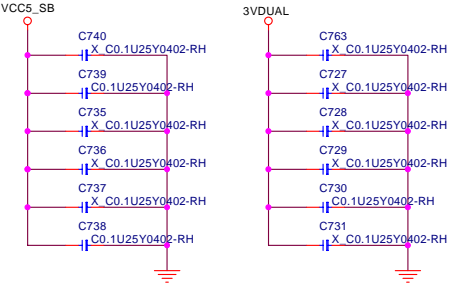


					SB600		SB460	
	AC_SDOUT	RTC_CLK	PCI_CLK4	PCI_CLK6	PCI_CLK0	PCI_CLK1	PCI_CLK0	PCI_CLK1
PULL HIGH	USE DEBUG STRAPS	INTERNAL RTC DEFAULT	USE INT. PLL48	CPU IF=K8 DEFAULT	ROM TYPE: H, H = PCI ROM H, L = SPI ROM L, H = LPC ROM L, L = FWH ROM	DEFAULT	ROM TYPE: H, H = PCI ROM H, L = LPC I ROM L, H = LPC II ROM L, L = FWH ROM	DEFAULT
PULL LOW	IGNORE DEBUG STRAPS DEFAULT	EXTERNAL RTC	USE EXT. 48MHZ DEFAULT	CPU IF=P4	NOTE: FOR SB460, PCI_CLK[8:7] ARE CONNECTED TO SUBSTRATE BALLS PCI_CLK[1:0]			

For EMI



For EMI

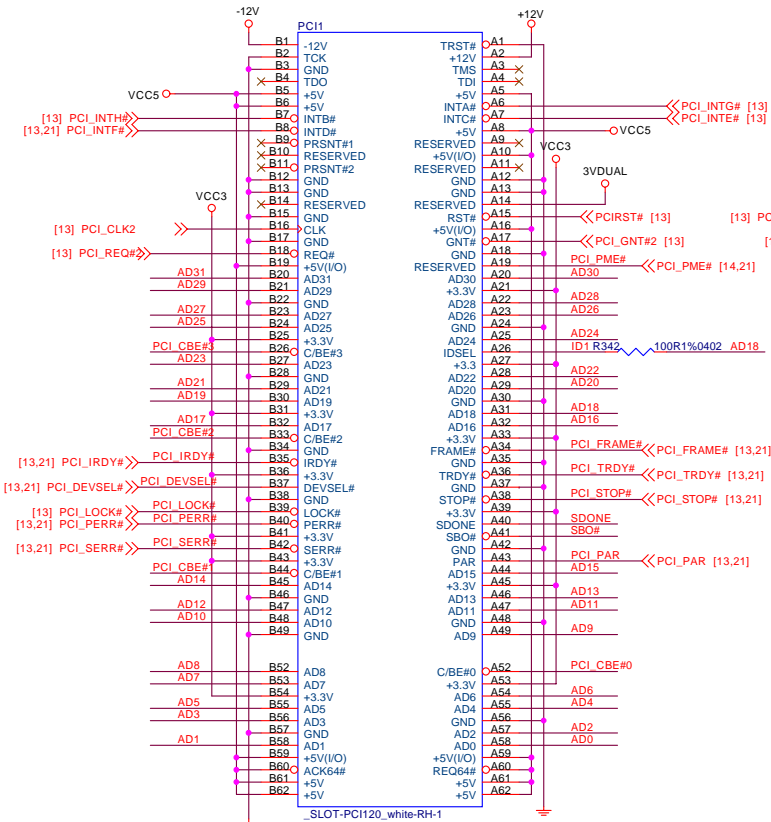


MICRO-START INTL CO.,LTD.

Title			SB600 STRAPS	
Size	Document Number	Rev		
Custom	MS-7297	21		
Date:	Thursday, December 14, 2006	Sheet	17	of 35

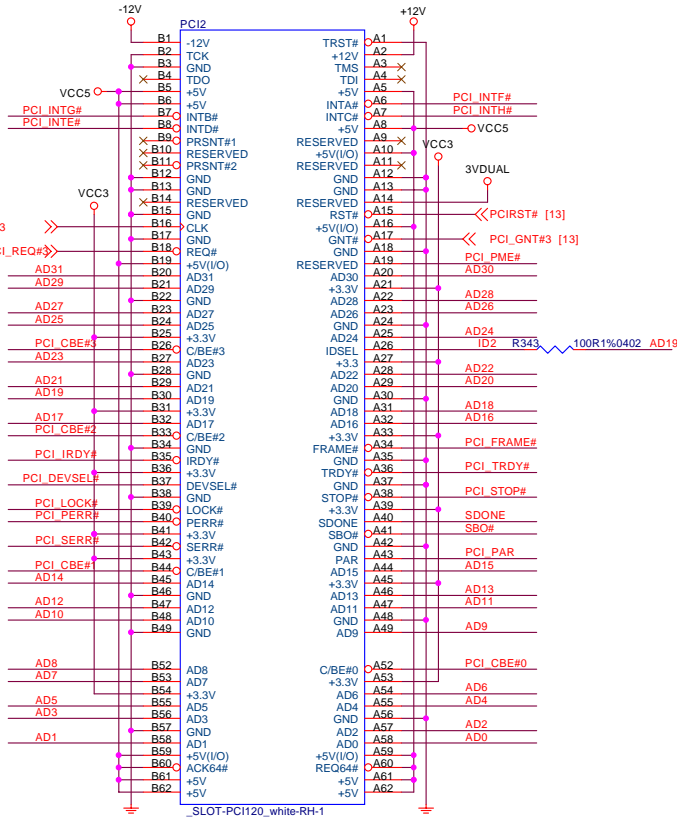
[13,21] AD[31..0] >> AD[31..0]  
[13,21] PCI\_CBE#[3..0] >> PCI\_CBE#[3..0]

### PCI SLOT 1 (PCI VER: 2.2 COMPLY)



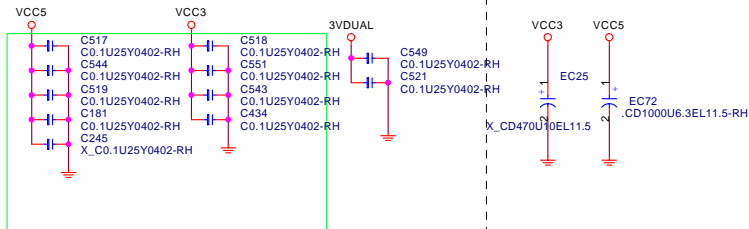
IDSEL = AD18  
MASTER = PCI\_REQ#2  
PCI\_GNT#2

### PCI SLOT 2 (PCI VER: 2.2 COMPLY)



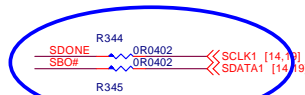
IDSEL = AD19  
MASTER = PCI\_REQ#3  
PCI\_GNT#3

### PCI SLOT DECOUPLING CAPACITORS

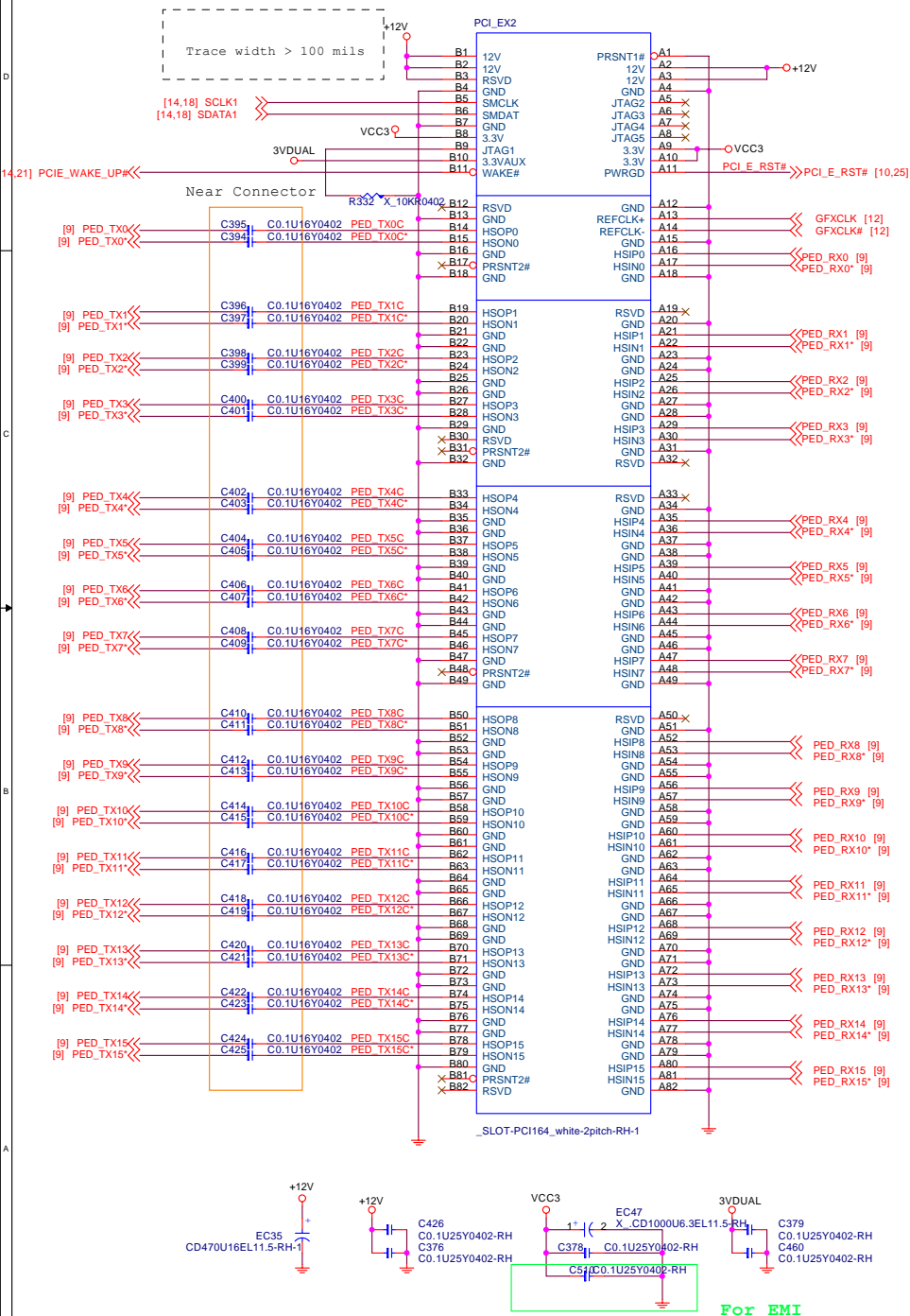


For EMI

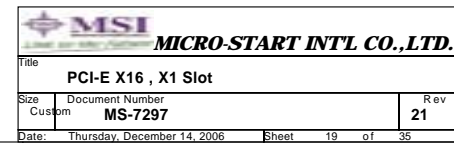
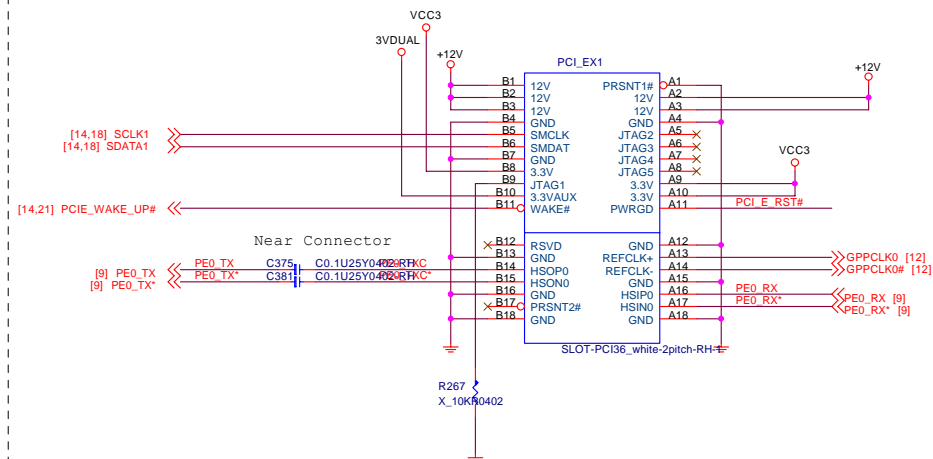
### PCI PULL-UP / DOWN RESISTORS



## PCI EXPRESS\_16

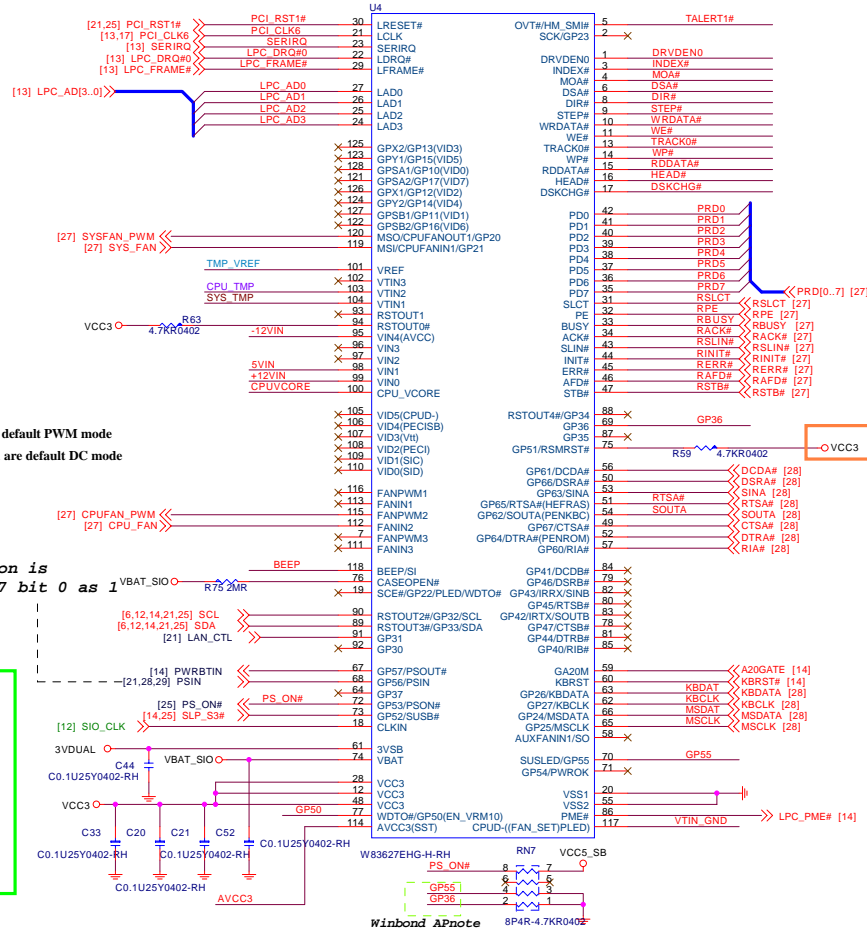


PCI-Express x1 SLO T 1

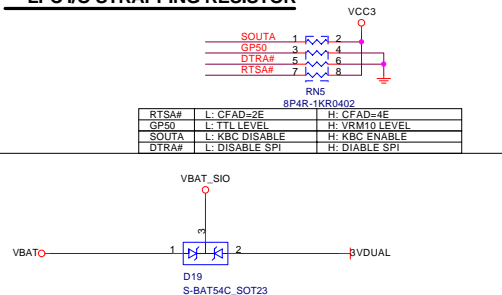


## Super I/O

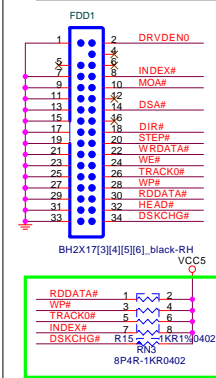
**LPC SUPER I/O W83627EHG**



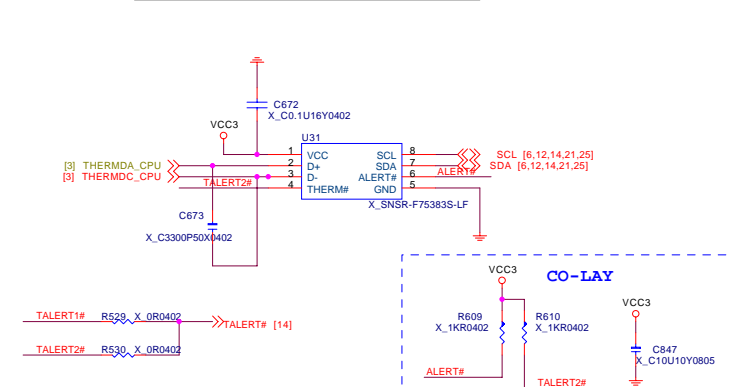
## LPC I/O STRAPPING RESISTOR



## FLOPPY CONNECTOR

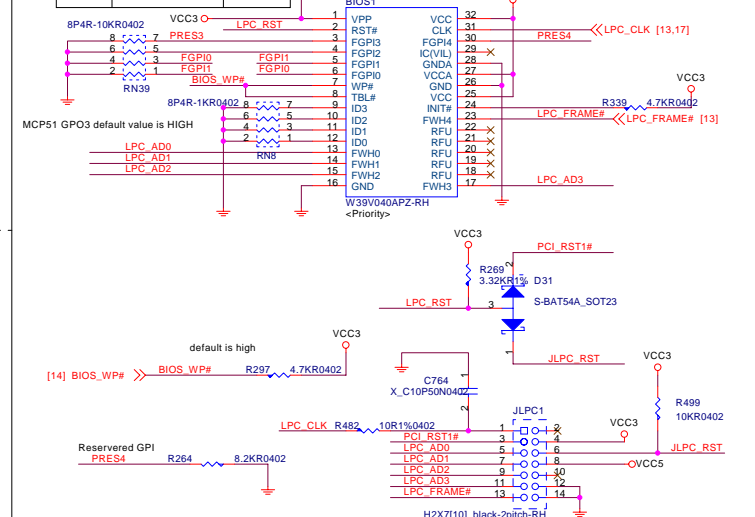


## CPU TEMPERATURE SENSOR

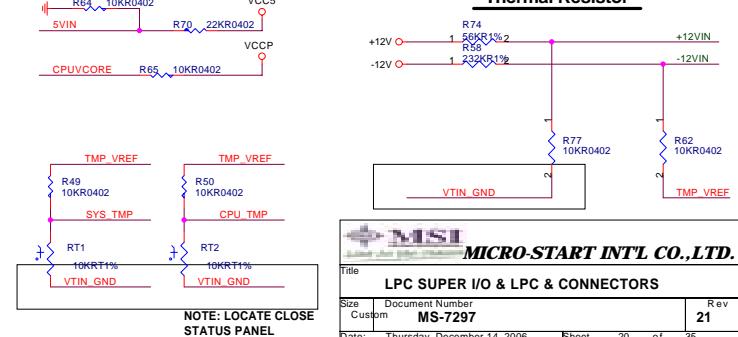


## BIOS PROTECT BLOCK

BIOS Update Config.		
HIGH	Un_protected	Default
LOW	Protected	

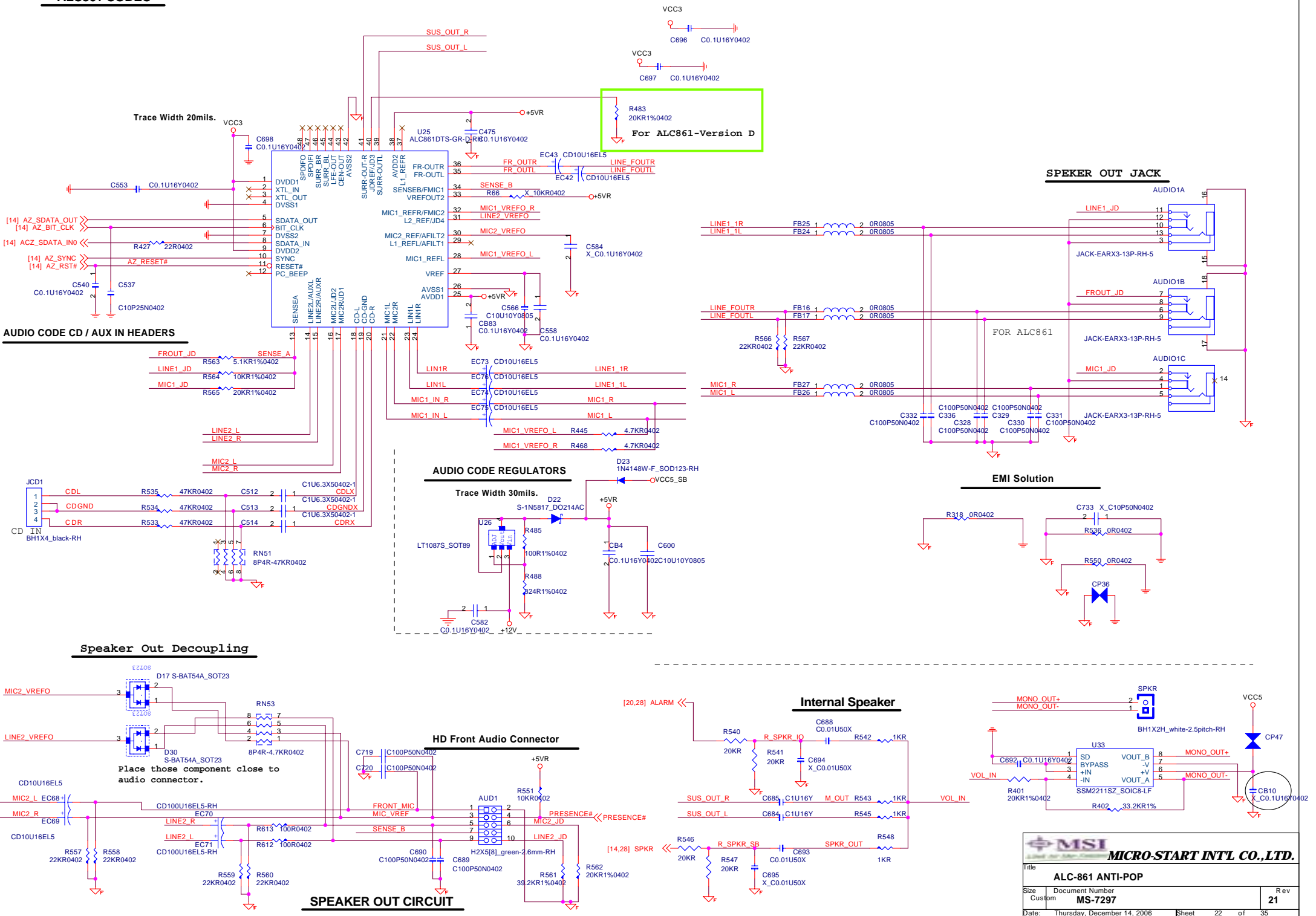


### Thermal Resistor

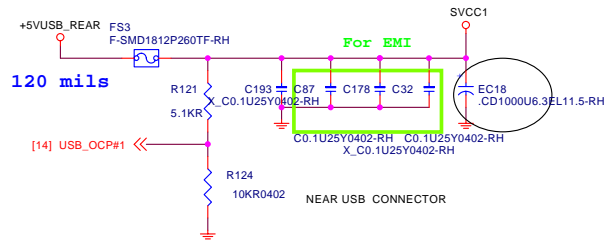




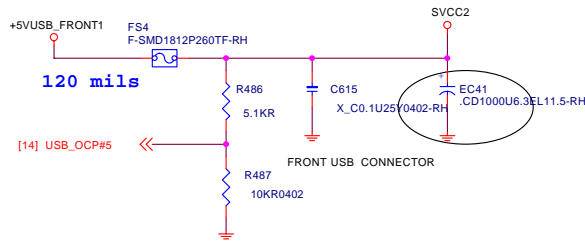
## ALC861 CODEC



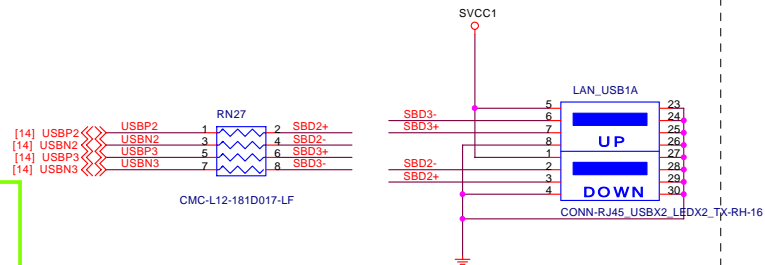
## POWER CIRCUIT FOR USB PORT 0,1



## POWER CIRCUIT FOR USB PORT 4,5,6,7



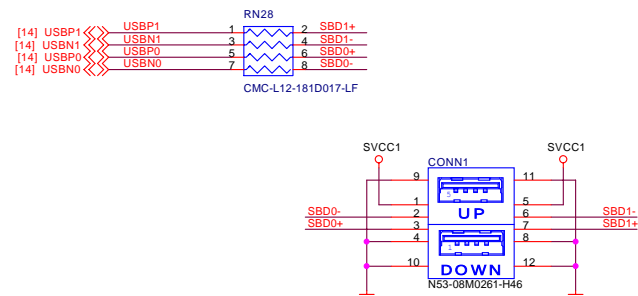
## REAR PANEL USB CONNECTOR FOR USB PORT 0,1



### NEAR USB CONNECTOR

22 / 7.5 / 7.5 / 7.5 / 22 / 7.5 / 7.5 / 7.5 / 22

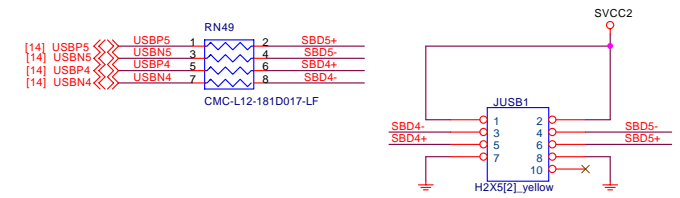
## REAR PANEL USB CONNECTOR FOR USB PORT 2,3



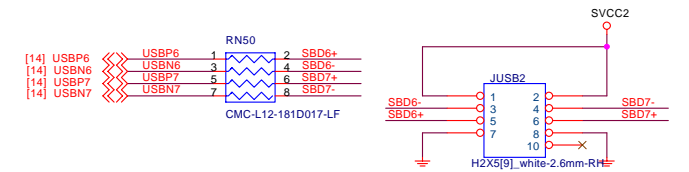
22 / 7.5 / 7.5 / 7.5 / 22 / 7.5 / 7.5 / 7.5 / 22

## FRONT PANEL USB CONNECTOR FOR USB PORT 4,5

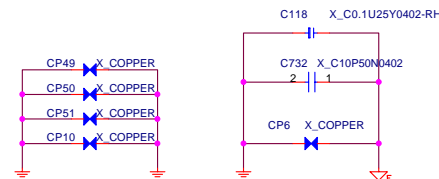
Reserved, can be taken off riser card within bead



## FRONT PANEL USB CONNECTOR FOR USB PORT 6,7



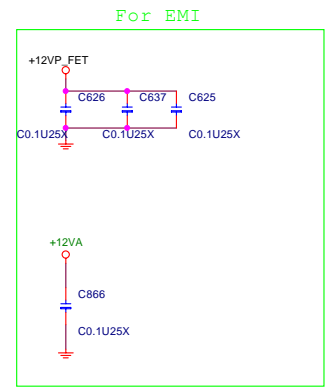
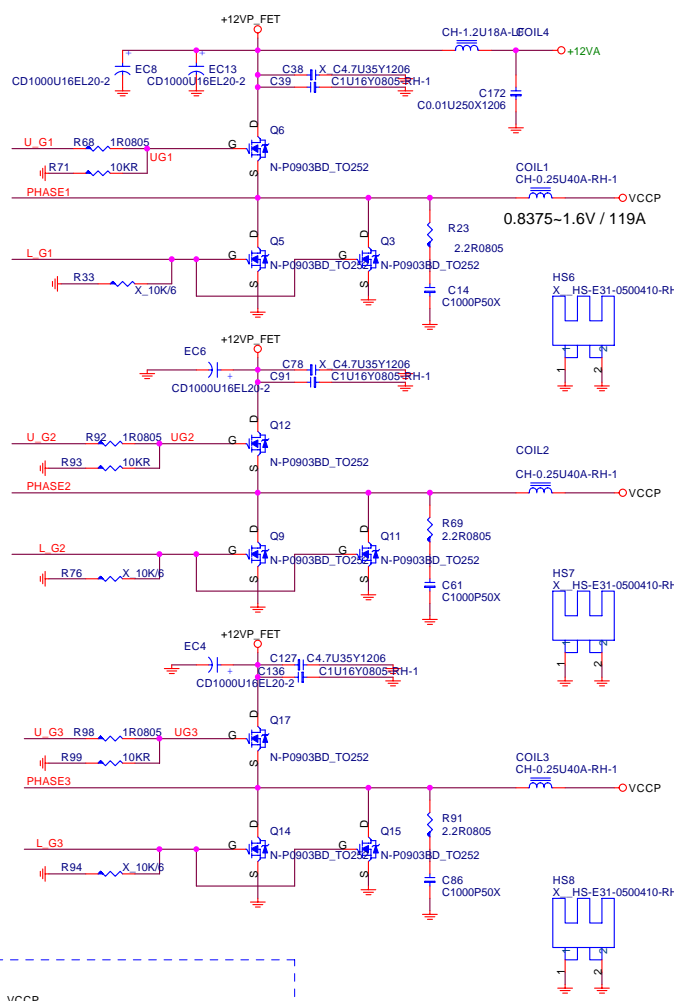
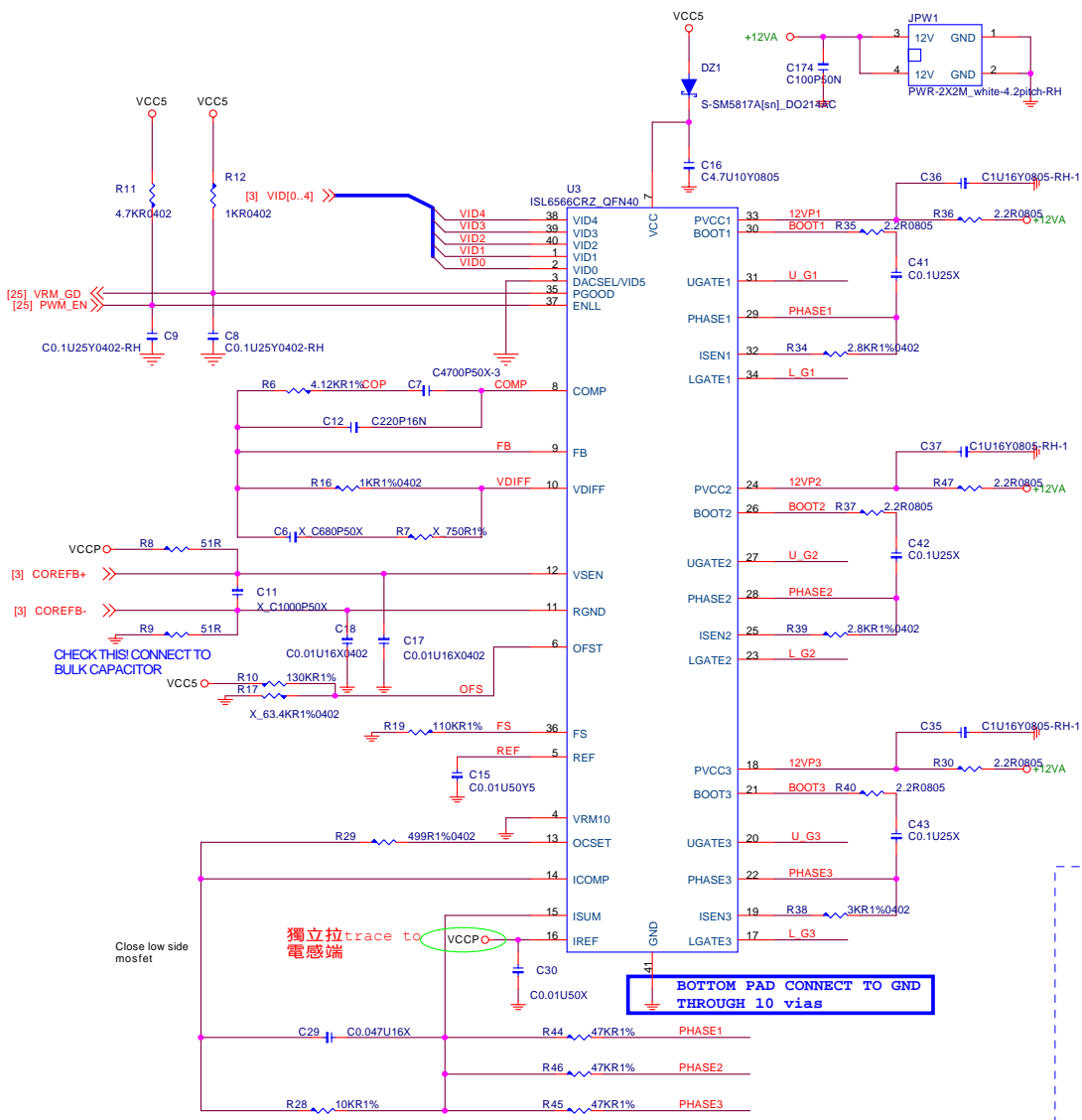
## EMI TEST



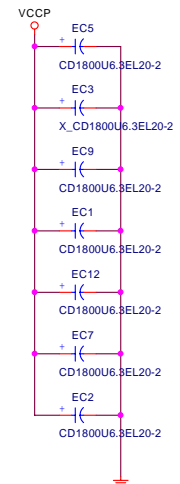


# Voltage Regular Module

IPF06N03LA Rds(on)=8.7mΩ (@4.5V, 30A), Vgs(on)=1.2~2V, Id=50A, Ciss=3110pf, Qg=10nC, Vds=25V, Vgs=±20V  
C100U2SP ESR<13mΩ, Ripple cur.<2.7A, LC<12uA, 105C  
.CD3300U6.3EL25 ESR<12mΩ, Ripplecur.<2800mA, 105C, longlife3000hrs, KZGSeries  
560u\_2.5V ESR=6mΩ, Ripplecur.=4400mA, Lc.<500uA, 105C/2000hrs  
1800UF/6.3V ESR<12mΩ, Ripplecur.<2350mA, 105C, longlife change from 2000hrs to 3000hrs ,KZJ series  
0.6uH/40A 0.6u/20%, Isat=40A, Rdc=1.2m ohm, PEW wire  
CH-1.2U18A 1.2u/20%, Dip-2/vertical17.5mm, 1.2ψ/5.5turns, 18A



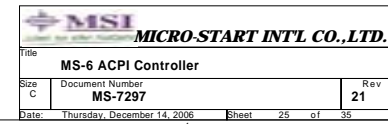
EL Capacitors



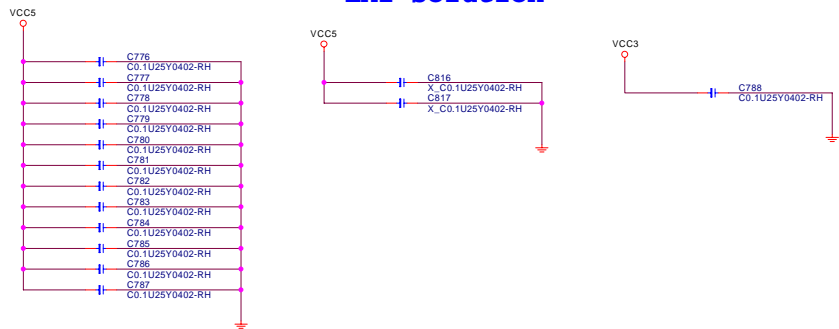
MOSFET Heatsinks



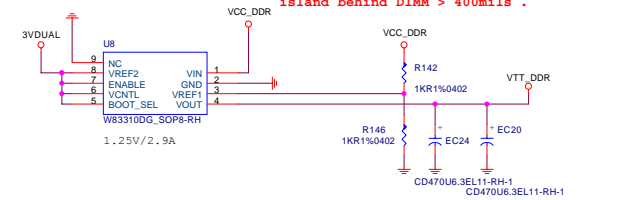
THESE OUTPUT AND INPUT PIN MUST  
PULL HIGH



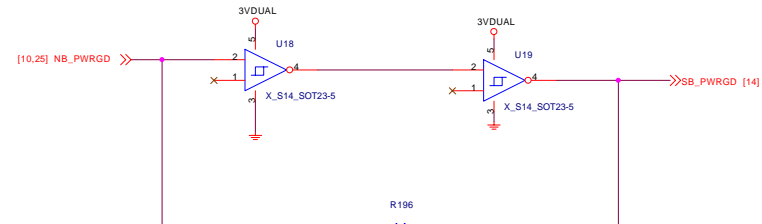
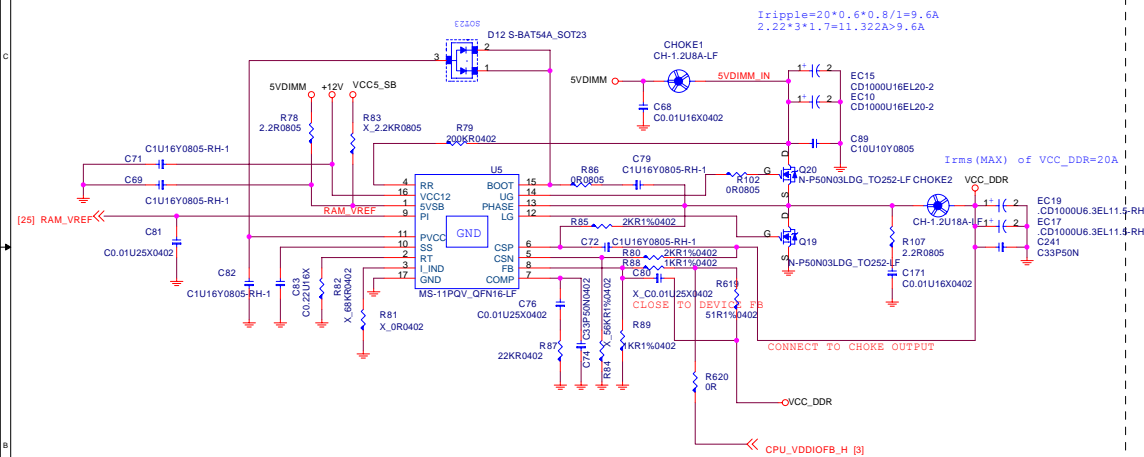
## EMI solution



## DDR VTT Power



## DDR II 1.8V POWER





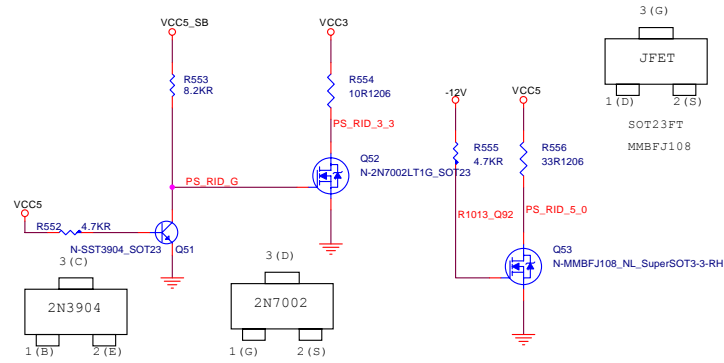
## Intel Front Panel



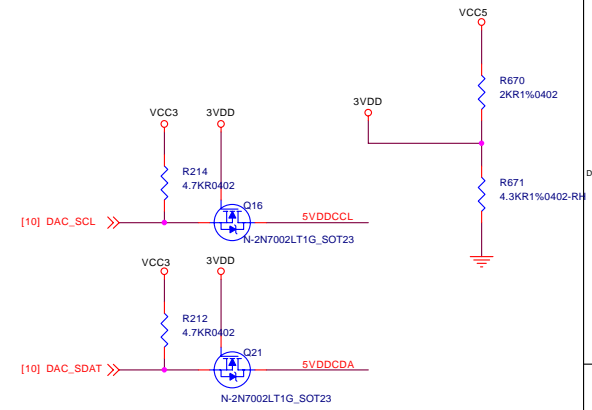
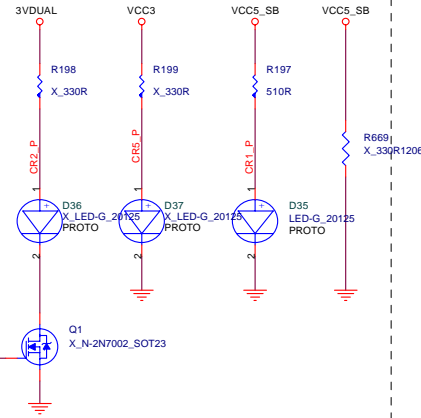
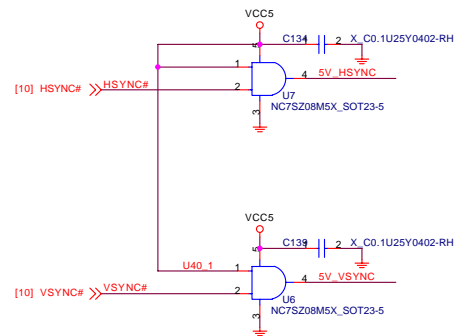
Title			
ATX connector / Front Panel/COM1/KB			
Size	Document Number	Rev	
Custom	MS-7297	21	
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# Near ATX POWER Connector

## BLEED-OFF CIRCUIT

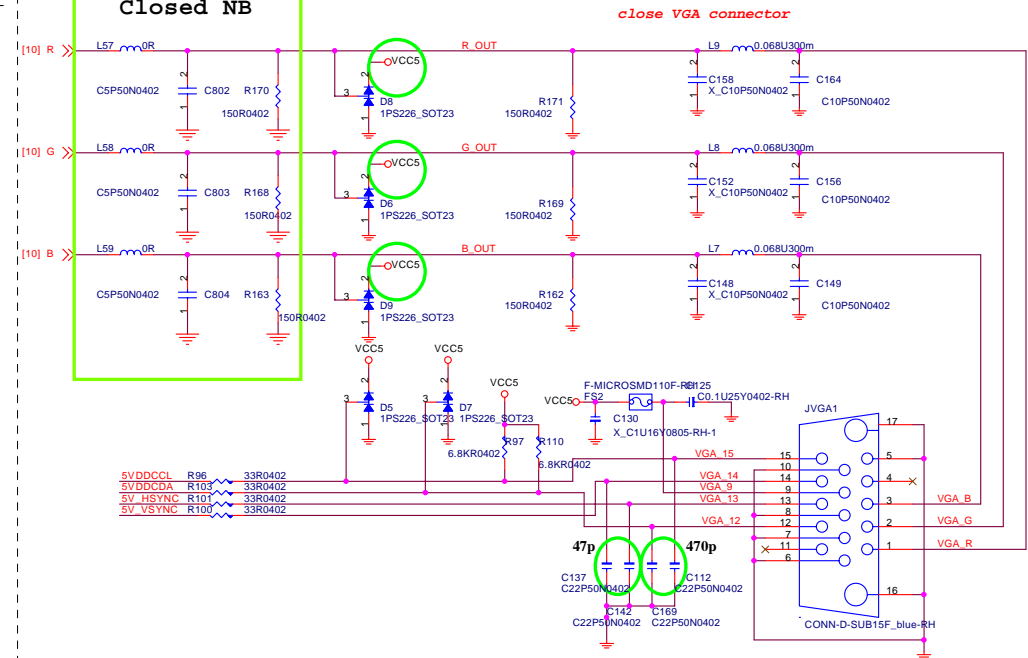


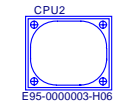
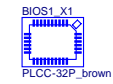
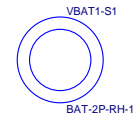
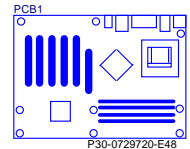
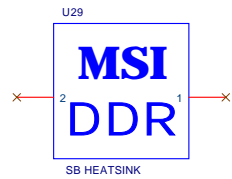
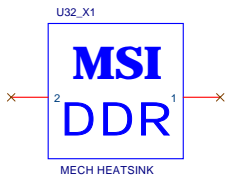
DESIGN NOTE: THIS CIRCUIT IS USED TO BLEED OFF 5.0V & 3.3V



## VGA CONNECTOR

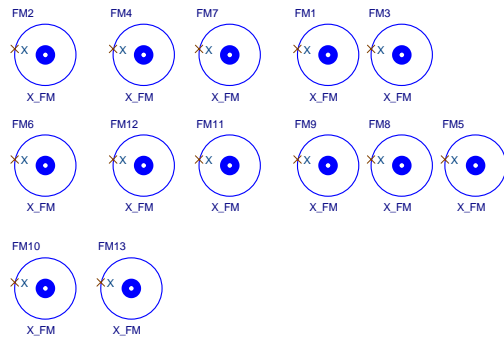
### Closed NB



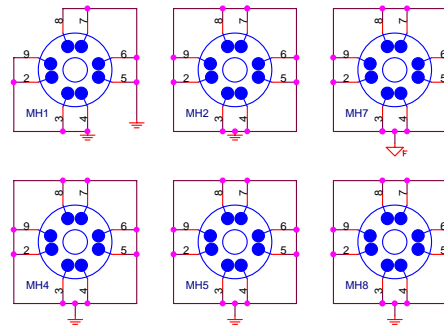


PF0-0729711-E48, 競華, 107, 寶安恩斯-贈 (MSIS)  
PF0-072971-G37, 精成, 107, 寶安恩斯通廠 (MSIS)

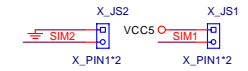
### Optics Orientation Holes



### Mounting Holes



### Simulation



### Model option table

Model type	Function	BOM Config	ERP BOM No.
MS-7297	RS485+SB600+RTL8110SB+ALC861+2PCI+u-ATX +2PS2+8USB+1COM+VGA+1Audio+LPT+RJ45	cfg-7297-0A	601-7297-01S